**Dept. of CSE, Bennett University**

**Digital Design – ECSE 108L**

**Lab Assignment – 09**

**Sequential Circuits: Counter**

**Ques 1.** Design behavioral code of edge triggered T flip flop.

Ques 2. Design structural UP counter 1-15 using T flipflop designed in ques 1.

Ques 3. Design DOWN counter using T positive triggered FF.